

**Amendments to the Specification:**

Please replace paragraph beginning on page 1, line 36 to page 2, line 15 with the following amended paragraph:

The aforementioned U.S. Patent Application Serial No. 10/075,094 is itself a continuation-in-part of the following U.S. Patent Applications: U.S. Patent Application Serial No. 09/821,342, Attorney Docket No. SILA:072, titled "Partitioned Radio-Frequency Apparatus and Associated Methods," filed on March 29, 2001; U.S. Patent Application Serial No. 09/708,339, Attorney Docket No. SILA:035C1, titled "Method and Apparatus for Operating a PLL with a Phase Detector/Sample Hold Circuit for Synthesizing High-Frequency Signals for Wireless Communications," filed on November 8, 2000; which is a continuation of U.S. Patent Application Serial No. 09/087,017, Attorney Docket No. SILA:035, filed on May 29, 1998, now U.S. Patent 6,167,245[[:]] . The entire text of each of the following listed patent applications is incorporated herein by reference: U.S. Patent Application Serial No. 10/075,122, Attorney Docket No. SILA:078, titled "Digital Architecture for Radio-Frequency Apparatus and Associated Methods," filed on February 12, 2002; U.S. Patent Application Serial No. 10/075,099, Attorney Docket No. SILA:097, titled "Notch Filter for DC Offset Reduction in Radio-Frequency Apparatus and Associated Methods," filed on February 12, 2002; and U.S. Patent Application Serial No. 10/074,676, Attorney Docket No. SILA:098, titled "DC Offset Reduction in Radio-Frequency Apparatus and Associated Methods," filed on February 12, 2002; ~~the entire text of each of the foregoing listed patent applications being incorporated herein by~~ reference.